

# WEST Search History

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DATE: Wednesday, May 02, 2007

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<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<input type="checkbox"/>	L18	L17 and l16 and l8	9
<input type="checkbox"/>	L17	((adjust\$3 or control\$3 or fix\$3) near2 (gain or amplitude or power\$)) and ((control\$3 or adjust\$3 or fix\$3) near2 phase\$) and (serial near2 parallel near3 convert\$3)	662
<input type="checkbox"/>	L16	332/106.ccls. or 332/107.ccls. or l5	4142
<input type="checkbox"/>	L15	(332/106.ccls. or 332/107.ccls. or l5)and l13	0
<input type="checkbox"/>	L14	332/106.ccls. or 332/107.ccls. or l5 and l13	344
<input type="checkbox"/>	L13	((adjust\$3 or control\$3 or fix\$3) near2 (gain or amplitude or power\$)) and ((control\$3 or adjust\$3 or fix\$3) near2 phase\$) and (trellis near2 (cod\$3 or modulat\$3)) and (serial near2 parallel near3 convert\$3)	28
<input type="checkbox"/>	L12	(trellis near2 (cod\$3 or modulat\$3) or (bit near2 mapp\$3)) same ((pre adj distortion\$) or predistortion)	5
<input type="checkbox"/>	L11	((adjust\$3 or control\$3 or fix\$3) near2 (gain or amplitude or power\$)) and ((control\$3 or adjust\$3 or fix\$3) near2 phase\$) and (trellis near2 (cod\$3 or modulat\$3)) and ((pre adj distortion\$) or predistortion)	19
<input type="checkbox"/>	L10	((adjust\$3 or control\$3 or fix\$3) near2 (gain or amplitude or power\$)) same ((control\$3 or adjust\$3 or fix\$3) near2 phase\$) same (trellis near2 (cod\$3 or modulat\$3))	11
<input type="checkbox"/>	L9	L6 and L8	164
<input type="checkbox"/>	L8	@ad<=20030714	24484021
<input type="checkbox"/>	L7	@ad<=-20030714	0
<input type="checkbox"/>	L6	L5 and L4	215
<input type="checkbox"/>	L5	(375/295).ccls. or (375/296).ccls. or (375/297).ccls. or (332/117).ccls. or (332/123).ccls. or (332/124).ccls. or (332/125).ccls.	3850
<input type="checkbox"/>	L4	((adjust\$3 or control\$3 or fix\$3) near2 (gain or amplitude or power\$)) same ((control\$3 or adjust\$3 or fix\$3) near2 phase\$)	20004
<input type="checkbox"/>	L3	((adjust\$3 or control\$3 or fix\$3) near2 (gain or amplitude or power\$)) same ((control\$3 or adjust\$3 or fix\$3) near2 phase\$) same (convert\$3 near3 (serial near2 parallel))	6
<input type="checkbox"/>	L2	(control\$3 near2 (gain or amplitude or power\$)) same (control\$3 near2 phase\$) same (convert\$3 near3 (serial near2 parallel))	5
<input type="checkbox"/>	L1	(predistor\$3) same (control\$3 near2 (gain or amplitude or power\$)) same (control\$3 near2 phase\$) same (convert\$3 near3 (serial near2 parallel))	0

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- #1 (((predistor\*) and (multi <near/2> bit\* <near/2> (buffer\* <or> memor\*)))<in>metadata)
- #2 (((predistor\*) <and> (multi\* <near/2> bit\* <near/2> (buffer\* <or> memor\*)))<in>metadata)
- #3 (((predistor\*) <and> (serial <near/3> parallel))<in>metadata)

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